

Notice of References Cited	Application/Control No. 10/751,523		Applicant(s)/Patent Under Reexamination YOSHIDA, TAKAKI	
	Examiner Phallaka Kik		Art Unit 2825	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,047,967 A	09-1991	Sander et al.	702/79
*	B	US-5,903,476 A	05-1999	Mauskar et al.	716/1
*	C	US-6,345,379 B1	02-2002	Khouja et al.	716/4
*	D	US-6,370,674 B1	04-2002	Thill, Michel	716/4
*	E	US-6,480,815 B1	11-2002	Olson et al.	703/14
*	F	US-6,615,395 B1	09-2003	Hathaway et al.	716/6
*	G	US-2003/0212538 A1	11-2003	Lin et al.	703/14
*	H	US-2003/0226122 A1	12-2003	Hathaway et al.	716/2
*	I	US-2006/0095872 A1	05-2006	McElvain et al.	716/001
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
	U	Bobba et al., "Simultaneous Switching Noise in CMOS VLSI Circuits", 1999 Southwest Symposium on Mixed-Signal Design, 11 April 1999, pp. 15-20.			
	V				
	W				
	X				

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.